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Computer Organization and Design RISC-V Edition: The ...

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Computer Organization & Design: The Hardware/Software ...

Computer Organization and Design THE HARDWARE/SOFTWARE INTERFACE David A. Patterson University of California, Berkeley John L. Hennessy Stanford University With a contribution by Peter J. Ashenden James R. Larus Daniel J. Sorin Ashenden Designs Pty Ltd Microsoft Research Duke University AMSTERDAM • BOSTON • HEIDELBERG • LONDON

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This best selling text on computer organization has been thoroughly updated to reflect the newest technologies. Examples highlight the latest processor designs, benchmarking standards, languages and tools. As with previous editions, a MIPS processor is the core used to present the fundamentals of hardware technologies at work in a computer system.

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Hennessy has a history of strong interest and involvement in college-level computer education. He co-authored, with David A. Patterson, two well-known books on computer architecture, Computer Organization and Design: the Hardware/Software Interface and Computer Architecture: A Quantitative Approach, which introduced the DLX RISC

John L. Hennessy - Wikipedia

Computer Organization and Design, Third Edition: The Hardware/Software Interface, Third Edition (The Morgan Kaufmann Series in Computer Architecture and Design) by David A. Patterson, John L. Hennessy. Morgan Kaufmann. Paperback. POOR. Noticeably used book. Heavy wear to cover. Pages contain marginal notes, underlining, and or highlighting.

9781558606043 - Computer Organization and Design, Third ...

Computer Architecture: A Quantitative Approach is a tour-de-force on several levels. The book is a masterpiece of technical writing -- Hennessy and Patterson's clear, direct style is absorbing and effective, and their enthusiasm for their subject is contagious. The design and production, too, are impeccable.

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